

## TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

<b>Title of Invention</b>	<b>DEFECT DIAGNOSIS FOR SEMICONDUCTOR INTEGRATED CIRCUITS</b>							
Application Number :								
Date :								
First Named Applicant:	James W. Adkisson							
Confirmation Number:								
Attorney Docket Number:	BUR920040092US1							
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<table border="1"><tr><td>Submitted By:</td><td>Elec. Sign.</td><td>Sign. Capacity</td></tr><tr><td>Richard M. Kotulak Esq. Registered Number: 27712</td><td>/Richard M. KOTULAK, Reg. #27712/</td><td>Attorney</td></tr></table>			Submitted By:	Elec. Sign.	Sign. Capacity	Richard M. Kotulak Esq. Registered Number: 27712	/Richard M. KOTULAK, Reg. #27712/	Attorney
Submitted By:	Elec. Sign.	Sign. Capacity						
Richard M. Kotulak Esq. Registered Number: 27712	/Richard M. KOTULAK, Reg. #27712/	Attorney						

Documents being submitted:	Files
us-ids	BUR920040092US1-usidst.xml us-ids.dtd us-ids.xsl
us-assignment	BUR920040092US1-usassn.xml us-assignment.xsl us-assignment.dtd BUR920040092US1Assign1of3.tif BUR920040092US1Assign2of3.tif BUR920040092US1Assign3of3.tif
us-request	BUR920040092US1-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	BUR920040092US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-declaration	BUR920040092US1Dec1of3.tif
us-declaration	BUR920040092US1Dec2of3.tif
application-body	BUR920040092US1spec-trans.xml us-application-body.xsl application-body.dtd
	wipo.ent mathml2.dtd mathml2-qname-1.mod
	isoamsa.ent
	isoamsb.ent
	isoamsc.ent
	isoamsn.ent
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	isocyr2.ent
	isodia.ent
	isolat1.ent
	isolat2.ent
	isonum.ent
	isopub.ent
	mmlextra.ent
	mmlalias.ent
	soextblx.dtd
	BUR920040092US1FIG1of3.tif
	BUR920040092US1FIG2of3.tif
	BUR920040092US1FIG3of3.tif
us-declaration	BUR920040092US1Dec3of3.tif

Comments

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